Silicon Controlled Rectifier

- SCR is a Four layered device
- SCR is a Three junction device
- SCR is a Three terminal device
- SCR is a half controlled device
- SCR is a DC switch i.e in an SCR current only flow in one direction
- Anode(A) and Cathode(K) are Power terminals and Gate(G) is a Controlled terminal.



Silicon Controlled Rectifier



Silicon Controlled Rectifier

- P+ and N+ represents highly doped
- N- represents lightly doped
- P represents Moderately doped
- Highly doped elements contribute less in forming the depletion region and lightly doped elements contribute more in forming the depletion region.



Modes of Operation of SCR

• The three modes of operation of SCR are

1. Reverse Blocking Mode

2. Forward Blocking Mode

3. Forward Conducting Mode

Reverse Blocking Mode

- The Anode is connected to the negative terminal of the battery and the cathode is connected to the positive terminal of the battery, then it is said to be **Reverse Biased.**
- In this Condition, Junctions J1 and J3 are Reverse biased and Junction J2 is Forward biased.



Reverse Blocking Mode

- The voltage across J2 = 0 v (Short circuited)
- As the Junctions J1 and J3 are reverse biased, there will be an Open circuit at the junctions and hence **ideally** the current flowing in the circuit will be equal to zero i.e. I= 0 Amps.
- But in **Practical**, there exists a little reverse leakage current(very less).
- As the Junction J2 cannot block the voltage in the circuit, the entire voltage will be blocked by two remaining junctions J1 and J3.



Reverse Blocking Mode

- In Reverse blocking mode, J1 has more depletion width when compared to J3 and hence large amount of voltage will be blocked at Junction J1.
- When we increase the reverse voltage, at a critical breakdown level called Reverse Breakdown voltage(VBR), an avalanche breakdown will occur at the junctions J1 and J3 increasing the current sharply as shown in the figure..
- Under reverse Blocking mode, SCR behaves as an OFF switch.



Forward Blocking Mode

- In this region, the Anode is made positive with respect to the cathode.
- J1 and J3 are forward biased and J2 is reverse biased.
- The anode current is a small leakage forward current.



Forward Conduction Mode

- When the Anode to Cathode voltage is increased with the gate circuit kept open, avalanche breakdown occurs at Junction J2 at critical forward break-over voltage(VBO).
- The SCR now switches into a low impedance condition (high conduction mode).
- When a gate signal is applied, the SCR turns –ON before VBO is reached.
- The forward voltage at which the device switches to ON state depends upon the magnitude of gate current.
- Higher the gate current, lower is the forward breakover voltage(VBO).



SCR V-I Characteristics



SCR V-I Characteristics



Switching Characteristics

- The Static characteristics gives no indication as to the speed at which the SCR is capable of being switched from the forward blocking voltage to the conducting state and vice-versa.
- However, the transition from one state to other does not take place instantaneously, it takes a finite period of time.
- The total turn-on time, ton of the SCR is subdivided into three distinct periods, called the Delay time, Rise time and Spread time.
- These time periods are defined in terms of the waveforms of the anode voltage and current obtained in a circuit in which the anode-load consists of pure-resistance.

- These characteristics are also called Dynamic Characteristics.
- These characteristics deals with Turn-ON and Turn-OFF times.
- To Turn-ON the SCR, a forward biased voltage has been applied between Anode and cathode as shown in the figure.
- If we increase the gate current Ig, then SCR can reach the conduction state before reaching forward breakover voltage,VBO.
- To increase the gate current, an voltage of Vg is applied between cathode and cathode terminals.



- When we apply a gate voltage Vg, gate current Ig will start flowing.
- At this condition, initially the anode current Ia is very low and anode voltage Va is very high.
- As gate current increase from 90% to 100%, Ia increase from leakage current to 10% of its final value whereas the anode voltage falls from Va to 90% of Va.



Delay Time, ta

From the characteristics, Delay time can be defined as follows.

- It is the time during which the gate current reaches from 90% to its final value of Ig.
- It is the time during which the anode current reaches10% of its final value.
- It is the time during which anode voltage falls from Va to 90% of Va, where Va is the initial value of the anode voltage.



Rise Time, tr

From the characteristics, Rise time can be defined as follows.

- It is the time required for the anode current to rise from 10% to 90% of its final value.
- It is the time required for the anode voltage to fall from 90% to 10% of its initial value.
- This time is inversely proportional to the magnitude of the gate current and its build up rate.
- The rise time tr can be minimized if high and steep current pulses are applied to the gate.



Spread Time, ts

From the characteristics, Spread time can be defined as follows.

- It is the time required for the anode current to rise from 90% to 100% of its final value.
- It is the time required for the anode voltage to fall from 10% to forward conducting voltage(1 to 1.5v).
- Turn-ON time is the sum of the delay time, rise time and spread time.
- This is typically of the order of 1 to 4 micro seconds and depends upon the anode current circuit parameters and the gate signal wave shapes.



- Once SCR Turn-ON, there will be charge carriers present at all the three junctions.
- To Turn-OFF the SCR, we need to remove the complete charge carriers across the three junctions.
- The time required to remove the charge carriers from the junctions J1 and J3 is called Reverse Recovery time(trr).
- The time required to remove the charge carriers from the junction J2 is called Gate Recovery time(tgr).



- Turn OFF time is the sum of the reverse recovery time and the gate recovery time.
- To turn-OFF the SCR, the anode current should be decreased below the holding current.
- The anode current decrease with a rate of dI/dt and reaches to a negative maximum value and then increase to reach steady state value and finally attain zero value.
- It reaches negative maximum because of the presence of charge carriers at the junctions J1, J2 and J3.



- As the anode current reaches negative maximum, the anode voltage has a spike and after the removal of charge carriers at the junctions J1 and J3, it reaches to zero as shown in the figure.
- The SCR is OFF but there are some charge carriers present at the junction J2.
- To make it completely OFF, we have to remove the charge carriers present at the junction J2 and the time required for removing the charge carriers at the junction J2 is called Gate recovery time(tgr).



• The time required from anode current reaches to zero to anode voltage reaches to zero is called the Circuit Turn-OFF time(tc).



- In an SCR, the gate is connected to the cathode through a PN junction and resembles a diode. Therefore, the V-I characteristics of a gate is similar to a diode but varies considerably in units.
- The circuit which supplies firing signals to the gate must be designed :

1. To accommodate these variations

- 2. Not to exceed the maximum voltage, and power capabilities of the gate
- 3. To prevent triggering from false signals or noise, and

4. To assure desired triggering.

• The design specification pertaining to gate characteristics are usually provided by the manufacturers.

Let

- Vs= Gate sourse voltage
- Is= Gate source current
- Rs= Gate source resistance
- Vg= gate voltage or Trigger voltage
- Ig= Gate current
- Pg(avg) = Vg*Ig



- Figure shows an SCR in forward blocking mode and to trigger the SCR, an voltage "Vg" is applied between the gate and cathode terminals and hence a current, "Ig" flow in the circuit.
- As the Junction "J3" is present in between the gate and cathode, the gate characteristics are related to this junction only.
- Hence we need to draw the forward bias characteristics of a PN junction at J3.



- In a highly doped junction, the breakdown strength is low and thereby fast breakdown occurs and hence Maximum amount of current will flow.
- Hence the change in the current is fast and the change in the voltage is slow (curve-1).
- For a lightly doped junction, the change in the current is slow and the change in the voltage is fast (curve-2).



- Represent Ig(max) and Vg(max) values in the characteristics.
- Gate power, Pg= Vg*Ig= constant. This represent a characteristic of Rectangular Hyperbola.
- In order to provide the power to the gate circuit, a practical voltage source is needed.



Draw the Load line:

From the circuit,

Ig=Is.....(1)

 $V_s = I_s * R_s + V_g \dots (2)$

Substitute equation(2) in equation(1),

 $Vs = Ig * R_s + Vg \dots (3)$

We can draw the load line by using the equation (3).



Draw the Load line:

Case 1:

Put Vg=0 Vs=Ig*RsIg=Vs/Rs....(4)

P represent the "Vs/Rs" in equation (4).

Case 2:

Put Ig=0 \bigvee Vs= Vg.....(5)

Q represent the value of "Vs" in equation (5).



Draw the Load line:

- Now join the points **P** and **Q**.
- The line obtained is called **"Load Line"**.
- The slope of the line is negative and is equal to Rs.

 $Vg = Vs - Ig * Rs \dots (6)$

The above equation is compared with the line equation

y=mx+C





- The SCR will operate in between the minimum and maximum limits.
- Hence Vg(min) and Ig(min) shown in the figure represent the minimum values of voltage and current below which the device cannot operate.
- If the device operates above the maximum limits, then the device will be damaged.
- The operating region is shown in the diagram with shaded lines.



Power MOSFET

- Power MOSFET : Power Metal oxide semiconductor Field effect transistor
- It has high switching frequencies of about 100 KHz
- Low input current
- Voltage controlled device
- High switching speed
- They do not have the problem of second breakdown
- High input impedance



Power MOSFET

- Three terminals i.e., Gate , Drain and Source.
- SiO2 layer creates the Capacitance.
- Source and Drain are connected to the "n+" layers which are highly doped.
- The drift layer (n- region) improves switching speed and voltage handling capacity.



Working of Power MOSFET:

- When we apply the voltage Vgs, SiO2 layer acts as a Capacitor and hence induces +ve charge and -ve charge on the plates as shown in the figure.
- If we increase Vgs, -ve charge generation increases and finally it forms a channel.
- This channel is called Induced channel or Inversion layer.



Working of Power MOSFET Contd...,:

- Now connect the drain and source to a battery with the polarities as shown in the figure.
- In that case, drain current flow from the drain to the source.
- If there is no channel i.e., Induced channel which is formed due to increase in Vgs voltage, then there will be a depletion region in between both n+ layers and hence it does not allow the drain current to flow.



Working of Power MOSFET Contd...,:

- Hence for the flow of drain current, there should be the induced channel and for this, we should increase
 Vgs voltage.
- Hence Power MOSFET is a
 Voltage Controlled
 device.



Characteristics of Power MOSFET

Transfer Characteristics



Output Characteristics


Characteristics of Power MOSFET contd..,,

- In output characteristics, as we increase Vds voltage, initially drain current increases and this attains a constant value and after breakdown, there will be a high amount of flow of drain current.
- It was repeated for different values of **Vgs voltage** as shown in the figure.



Power IGBT

- It has the best combination quality of BJT and MOSFET
- It has high input impedance like MOSFET
- It has low ON state power loss as in BJT
- Three terminals i.e. Gate , Emitter and Collector





Power IGBT Contd..,

- The space in the figure represents "SiO2" layer which creates a Capacitor.
- It is because, it is insulated and hence it is a capacitor between
 gate and the semiconductor
 material.
- Voltage between Gate and Emitter is Vg volts.
- Voltage between Emittor and the collector is **Vce** volts.



Power IGBT Contd..,

- Initially connect the gate and the emitter terminal with a battery (Vg) and connect collector and emitter with a battery(Vce).
- Because of Vce voltage, i.e., as the collector is +ve with respect to the emitter, Junction J1 is forward biased and J2 is reverse biased.
- As J2 is reverse biased, the current will not flow from collector to the emitter.



Power IGBT Contd..,

• As we increase Vg b voltage, then the capacitor is formed inside the "SiO2" layer and it injects a negative charge over the regions as shown in the figure and hence the channel will be formed which gives a path to flow of current from collector to emitter.



Characteristics of Power IGBT

Transfer Characteristics



Output Characteristics



Turn-On Methods of an SCR

An SCR can be switched from a non conducting state to a conducting state in several ways and are described as follows.

1. Forward voltage triggering

2. Thermal triggering(Temperature triggering)

3. Radiation triggering(Light triggering)

4.dv/dt triggering

5. Gate triggering

Forward Conduction Mode of SCR





Static V-I Characteristics of SCR



Forward Voltage Triggering

- When anode-to-cathode forward voltage is increased with gate circuit open, the reverse biased junction J2 will have an avalanche breakdown at a voltage called Forward break over voltage VBO.
- At this voltage, the SCR changes from OFF state(high voltage with low leakage current) to ON state characterized by a low voltage across it with large forward current.
- The forward voltage drop across the SCR during the ON state is of the order of 1 to 1.5V and increases slightly with the load current.

Thermal Triggering(Temperature Triggering)

- Like any other semiconductor, the width of the depletion layer of an SCR decreases on increasing the junction temperature.
- Thus, in an SCR, when the voltage applied between the anode and cathode is very near to its breakdown voltage, the device can be triggered by increasing its junction temperature.
- By increasing the temperature to a certain value(within the specified limits), a situation comes when the reverse biased junction collapses making the device conduct.
- This method of triggering the device by heating is known as the Thermal triggering method.

Radiation Triggering(Light Triggering)

- In this method, as the name suggests, the energy is imparted by radiation.
- The SCR is bombarded by energy particles such as Neutrons or Photons.
- With the help of this external energy, electron-hole pairs are generated in the device, thus increasing the number of charge carriers.
- This leads to instantaneous flow of current within the device and the triggering of the device.
- For radiation triggering to occur, the device must have high value of rate of change of voltage(dv/dt).
- LASCR and LASCS are the examples of this type of triggering.

dv/dt Triggering

- We know that with forward voltage across the anode and cathode of a device, the junctions J1 and J3 are forward biased, whereas the junction J2 becomes reverse biased.
- This J₂ has the characteristics of a capacitor due to charges existing across the junction.
- If a forward voltage is suddenly applied, a charging current will flow tending to turn the device ON.
- If the voltage impressed across the device is denoted by V, the charge by Q and the capacitance by C_j, then

 $ic = dQ/dt = d(Cj^*V)/dt = Cj^*dV/dt + V^*dCj/dt$

- The rate of change of junction capacitance may be negligible as the junction capacitance is almost constant.
- Therefore, if the rate of change of voltage across the device is large, the device may turn-on even though the voltage appearing across the device is small.

Gate Triggering

- By applying a positive signal at the gate terminal of the device, it can be triggered much before the specified break over voltage.
- The conduction period of the SCR can be controlled by varying the gate signal within the specified values of the maximum and minimum gate currents.
- For gate triggering, a signal is applied between the gate and the cathode of the device.

Turn-Off Methods of an SCR

The two methods by which an SCR can be turned off are as follows.

1. Natural Commutation

2. Forced Commutation

Commutation

- The term Commutation is basically means the transfer of current from one path to another.
- In SCR, this term is used to describe the process of transferring the current from one SCR to another.
- In general, it is not possible to for an SCR to turn itself OFF; the circuit in which it is connected must reduce the SCR current to zero to enable it to turn off.
- Commutation is the term to describe the methods of achieving this.

Natural Commutation

- The simplest and most widely use of method of commutation makes use of the alternating, reversing nature of ac voltages to effect the current transfer.
- We now that in ac circuits, the current always passes through zero every half cycle.
- As the current passes through natural zero, a reverse voltage will simultaneously appear across the device.
- This immediately turns-off the device.
- This process is called Natural commutation since no external circuit is required for this purpose.
- This method may use ac mains supply voltages or ac voltages generated by local rotating machines or resonant circuits.
- The line commutated converters and inverters comes under this category.

Forced Commutation

- Once SCRs are operating in the ON state, carrying forward current, they can only be turned OFF by reducing the current flowing through them to zero for sufficient time to allow the removal of charge carriers.
- In case of DC circuits, for switching off the SCRs, the forward current should be forced to zero by means of some external circuits.
- The process is called Forced Commutation and the external circuits required for it are called Commutation circuits.
- The components (inductance and capacitance) which constitutes the commutating circuits are called Commutating components.
- A reverse voltage is developed across the device by means of a commutating circuit that immediately brings the forward current in the device to zero, thus turning off the device.

Class A commutation

Class A commutation is also called Load

Commutation and the commutation is

Done by the load current.

The circuit consist of,

- Supply voltage ,Vs
- SCR
- Load Resistance, R
- Commutating elements L and C



Working of Class A commutation Circuit

class A commetation For an indudance system, Chagacteistic equation , s + Rs + 1 = 0 シリアテモ Compare with sit 25 wm s+ wn = 0 Undudamper $\omega_n = \frac{1}{1} \Rightarrow \omega_n = \frac{1}{1}$ $2 \epsilon_{\mu} \omega_{n} = \frac{R}{L}$ Max. Conduction time of scr. ⇒ 29(二)=~ twy = x => t = xwy $= \sum_{k=1}^{\infty} \frac{1}{2k} = \frac{R}{2L} \cdot \sqrt{Lc}$ => E = R C Turn-on time of scr a "d" fining Augle is, Y5/L te SCR can stay on during oto T $t = \frac{\pi - \lambda}{\omega_n \sqrt{1 - \lambda}}$

Working of Class A commutation Circuit

- Initially SCR is OFF.
- When we apply gate pulse, SCR will turn ON.
- The voltage across the capacitor will charge to supply voltage,Vs.
- Load current will flow in the circuit and increase till the capacitor will charge to supply voltage,Vs.
- As the SCR is ON, the voltage across the SCR will be Zero.



Working of Class A commutation Circuit

- Once the capacitor charge upto Vs, the polarities of the inductor will change and the energy stored in the inductor will discharge as like shown in the figure and hence it energize the capacitor and the capacitor will charge upto some extent further.
- The output current will start to decrease and reaches to zero and until it reaches zero,the SCR is in ON position and hence the voltage across the SCR will be zero.
- When the output current goes below the holding current, the SCR becomes OFF and the capacitor starts discharge and voltage across the SCR will start increase.



Output Waveforms of Class A commutation Circuit

• Initially SCR is OFF.

Zero.

- When we apply gate pulse, SCR will turn ON.
- The voltage across the capacitor will charge to supply voltage,Vs.
- Load current will flow in the circuit and increase till the capacitor will charge to supply voltage,Vs.
- As the SCR is ON, the voltage across the SCR will be



Output Waveforms of Class A commutation Circuit

- Once the capacitor charge upto Vs, the polarities of the inductor will change and the energy stored in the inductor will discharge as like shown in the figure and hence it energize the capacitor and the capacitor will charge upto some extent further.
- The output current will start to decrease and reaches to zero and until it reaches zero,the SCR is in ON position and hence the voltage across the SCR will be zero.
- When the output current goes below the holding current, the SCR becomes OFF and the capacitor starts discharge and voltage across the SCR will start increase.



Class B Commutation

Also called Resonant Pulse Commutation or Current Commutation.

- T1= Main SCR
- TA= Auxiliary SCR
- For resonant pulse generation, capacitor and inductor are connected.
- D=Diode

Assumptions:

- Load current is constant
- LC circuit is resonant in nature
- Capacitor is initially charged by supply voltage, Vs.



- Initially both SCRs are OFF.
- The flow of current in the circuit will be like as shown in the figure.



- As the capacitor charges up to supply voltage, the diode will comes into reverse biased and diode will get turn off and at this condition the polarities of the capacitor will be like as shown in the figure.
- The capacitor does not have any path to get discharge.



• SCR 1 is given a trigger signal to make it turn ON.

• The flow of current will be like as shown in the figure.

• But there is no path for the capacitor to get discharged.



• SCR A is given a trigger signal to make it turn ON.

• The flow of current which is due to capacitor voltage Vs will be like as shown in the figure.

• The polarities of the capacitor will change as like shown and hence the auxiliary SCR will come into Reverse bias and hence SCR A will turn OFF.



• As the polarities of the capacitor voltage is reversed and hence the auxiliary SCR will come into Reverse bias and hence SCR A will turn OFF.



- At this condition diode D will be forward biased and the flow of resonant current is like as shown in the figure.
- This current of capacitor will flow till the difference in the load current and the capacitor current will get zero.
- At this condition, the anode current in the SCR 1 will go below the holding current and hence SCR 1 will turn OFF.



RgA 3 , °T. Vc ic TI-ON TA-ON Troff -off

Output Waveforms of Class B Commutation

Class C Commutation

• It is also called Impulse Commutation.

• It is also called Complementary Commutation as to turn off one SCR, we should give gate signal to another SCR.



- SCR 1 is ON
- Current will flow in two loops as shown in the

figure.



- SCR 2 is ON
- Current will flow in two loops as shown in

figure.

• To turn off SCR 2, gate signal is applied to SCR 1.



Output Waveforms of Class C Commutation


- It is also called Voltage Commutation as the SCR commutation is done by voltage.
- It is also called Auxiliary Commutation.
- It is also called Parallel capacitor commutation

- T1=Main SCR
- TA=Auxiliary SCR
- D=Diode
- L=Inductor
- C=Capacitor
- Vs= Supply Voltage

Assumptions:

- Load current is constant
- Capacitor is initially charged by Vs.



- Initially both SCRs are OFF.
- As the diode D is in Reverse biased condition, the capacitor cannot charge.



- TA is ON.
- The flow of current is like as shown in the figure.
- The capacitor will charge due to the loop current flowing in the circuit with the polarities (+,-) and with the voltage Vs as shown in the figure.
- As soon as the capacitor is fully charged, TA turns OFF. It is due to the fact that as the voltage across the capacitor increases, the current through the TA decreases since capacitor C and TA forms a series circuit.



- T1 is ON.
- The flow of current will be in two loops as shown in the figure.
- Loop 2 is meant for discharging.
- In loop 2, Commutation current(capacitor discharges through) flows.
- After the capacitor has completely discharged, its polarity will be reversed(-,+).
- As the reverse discharge of the capacitor C will not be possible due to the blocking diode D. Here in this mode, T1 –ON, TA –OFF and Vc=-Vs.



- To turn OFF T1, TA will be ON.
- The capacitor voltage with polarities(-,+) will appear across T1 and hence it get reverse biased and T1 turns OFF.
- The flow of current is like as shown in the figure.
- This current will flow until the capacitor voltage charges from –Vs to =Vs.



• As soon as the voltage polarities of the capacitor changes to (+,-) as shown in the figure, the TA turns OFF.



Output Waveforms of Class D Commutation



Class D Commutation in one diagram



- In this commutation, a reverse voltage is applied to the current carrying SCR from ana external pulse source.
- Here the commutating pulse is applied through a pulse transformer which is suitably designed to have tight coupling between the primary and secondary.



- When the SCR T is triggered, the current flows through the load and the pulse transformer.
- When a pulse voltage is applied to the primary of the pulse transformer, the voltage induced in the secondary appears across the SCR T as a reverse voltage and turn it OFF.



- Since the induced pulse is of high frequency, the capacitor offers almost zero impedance.
- After SCR T is turned OFF, the load current decays to zero.



- It is also called Line Commutation.
- If the supply is an alternating voltages, the load current will flow during the positive half cycle.
- During the negative half cycle, the SCR will turn off due to negative polarity across it.
- The duration of the half cycle must be longer than the turn off time of the SCR.
- The maximum frequency at which this circuit can operate depends on the turn off time of SCR.



Snubber Circuits

- In most Power electronic circuits, protection is necessary against the effects of excessive rate of rise of forward voltage(dv/dt) across the devices, which can otherwise cause unintended breakover, leading to malfunction of the circuit and possible failure of the devices.
- The tendency to excessive dv/dt may arise from external cause such as the closing of main supply contactor, or from the operation of the circuit itself.
- dv/dt suppression is achieved by means of snubber circuits.
- The snubber circuit basically consists of a series connected resistor and capacitor placed on shunt with an SCR.
- A capacitor C across the SCR means that any high dv/dt appearing at the SCR terminals will set up an appropriate current(=Cdv/dt) in the capacitor.

Snubber Circuits

- The inductance in the circuit will severely limit the magnitude of the current to the capacitor and hence limit dv/dt.
- When the SCR turn-off, there is a brief pulse of reverse recovery current that rises to peak value, at which the device blocks.
- In the absence of RC snubber, the abrupt interruption of the reverse recovery current in the series inductance L, will cause transient Ldi/dt over voltages that may destroy the device itself or other semi conductors in the converter circuit.



Snubber Circuits

- If an RC Snubber circuit is connected across the SCR, the reverse recovery current can transfer to the RC path when the device blocks.
- The voltage across the RC path appears as an oscillatory reverse voltage across the semiconductor.
- A correctly designed Snubber will limit the amplitude of this reverse recovery voltage and will also limit it rate of rise.
- By delaying the build-up of reverse voltage across the semiconductor, the recovery losses in the device are also reduced.
- If the Snubber resistance is too small, excessive ringing will occur in the LCR circuit, and the reverse blocking voltage capability of the semiconductor may be exceeded.
- When the SCR turns On, the Snubber capacitor, C discharges into the SCR but the resistor R limits the discharge current and prevents excessive di/dt at turn-on.

Design of Snubber Circuit



Rectifiers

- Rectification is a process of converting an alternating current or voltage into direct current or voltage.
- This conversion can be achieved by a variety of circuits based on and using switches.
- The widely used switching devices are diodes, thyristors, power transistors, power MOS etc.
- The rectifier circuits can be broadly classified into three classes : Uncontrolled, Fully-controlled, and Half-controlled.
- An uncontrolled rectifier uses only diodes and the dc output voltage is fixed in amplitude by the amplitude of the ac supply.

Rectifiers

- The fully controlled rectifiers uses Thyristors as the rectifying elements and the dc output voltage is a function of the amplitude of the ac supply voltage and the point on wave at which the thyristors are triggered(called Firing angle Alpha).
- The half controlled rectifier contains a mixture of diodes and thyristors, allowing a more limited control over the dc output voltage level than fully controlled rectifier.



$$\frac{(-Q) \operatorname{Rethin} \operatorname{Wilt} \operatorname{R-load}}{\operatorname{Average} \operatorname{load} \operatorname{voltage}}$$

$$\frac{\operatorname{Average} \operatorname{load} \operatorname{voltage}}{\operatorname{Vo}(\operatorname{avg}) = \frac{1}{T} \int_{0}^{T} \operatorname{V_{0}}(t) \cdot \operatorname{durt}} \operatorname{Vo}(t) \cdot \operatorname{durt} = n = 1.$$

$$\operatorname{Time} \operatorname{period}, T = \frac{2\pi}{T} = \frac{2\pi}{T} = \frac{2\pi}{T} = 2\pi$$

$$\Rightarrow \operatorname{Vo}(\operatorname{avg}) = \frac{1}{2\pi} \int_{0}^{T} \operatorname{Vu}(\operatorname{Simut} \cdot \operatorname{durt})$$

$$= \frac{\operatorname{Vu}}{2\pi} \left[-\operatorname{cos}\operatorname{urt} \right]_{d}^{T}$$

$$= \frac{\operatorname{Vu}}{2\pi} \left[-\operatorname{cos}\operatorname{urt} \right]_{d}^{T}$$

$$= \frac{\operatorname{Vu}}{2\pi} \left[-\left(-\operatorname{cos}\operatorname{urt} \right) \right]_{d}^{T}$$



9) If the half-wave controlled spectifies
has a pundy sunishic load of "R"
and the delay acyle is
$$d = \frac{\pi}{3}$$
. Diference:
(a) Rectification efficiency (b) Form factor
(c) Ripple factor (d) Tonace for sca
(c) peak surves voltage for sca
(a) Rectification efficiency, $\eta = \frac{P_{dc}}{P_{ac}}$
(c) Sume, fac = de load power
 $= \frac{V_0^{\circ}(\alpha y)}{R}$
(c) Sums load power
 $= \frac{V_0^{\circ}(\alpha y)}{R}$
(c) Sums load power
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(c) Sums load power
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(c) Sums load power
 $= \frac{V_0^{\circ}(\gamma m_y)}{R}$
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 $(\alpha \cdot y g g V w_{0})^{\circ}$

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$$\frac{V_0(min)}{V_0(min)}$$

(c) Ripple factor (FF) = $\frac{V_0(min)}{V_0(min)}$
(c) Ripple factor (FF) = $\frac{V_0(min)}{V_0(min)}$
 $= \frac{0.485Vm}{0.237Vm} = 2.0332203.77.$
(c) Ripple factor (Rf) = $[(FF)^2 - 1]^{1/2}$
 $= (2.033)^2 - 1]^{1/2}$
 $= 1.77 = 1777.$
(d) Transform Willistin factor
TUF = $\frac{P_{dc}}{V_5(min)}$
 $TUF = \frac{P_{dc}}{V_5(min)}$
 $V_5(min) = V_0(min)$
 $V_5(min) = V_0(min)$

Single Phase half wave controlled rectifier with RL-Load without Free Wheeling Diode



Single Phase half wave controlled rectifier with RL-Load without Free Wheeling Diode

1-4 Half - wave Controlled Rectifier with RL-load ._ (At 277+d', cyain SCR is trajoned. Vs = Vo Vin Smeet VS @ Duning -ve half - cycle, cusigent Continues to flow till the ency, stored in the "L" NO. In the load guesister and a part of 12711 22 the energy is fed back to the Source. 24 Ttt TI Jo1 (Hence due to ency , stand in the Inductor, Current Continues to flow 2746 41 upla "A+d (a) At Tith, load Current is tero and due to regulie Supply voltage, T-OFF. 1121 71 w

Single Phase half wave controlled rectifier with RL-Load without Free Wheeling Diode



Single Phase half wave controlled rectifier with RL-Load with Free Wheeling Diode



Single Phase half wave controlled rectifier with RL-Load with Free Wheeling Diode

Single Phase full wave controlled rectifier










Single Phase full wave controlled rectifier-Center Tapped Configuration with RL-Load without Free wheeling Diode and Continuous Conduction Mode



Single Phase full wave controlled rectifier-Center Tapped Configuration with RL-Load without Free wheeling Diode and Continuous Conduction Mode

For appred Configuration
For antimums and the unode,

$$V_0(ay) = \frac{1}{T} \int_{0}^{T} V_0(t) \cdot dw t$$

 $= \frac{1}{T} \int_{0}^{T} V_0(t) \cdot dw t$
 $= \frac{1}{T} \int_{0}^{T+t} V_{th} Sin w t \cdot dw t$
 $= \frac{V_{th}}{T} \left[-Convt \right]_{A}^{T+tA}$
 $= \frac{V_{th}}{T} \left[-Convt \right]_{A}^{T+tA}$
 $= \frac{V_{th}}{T} \left[-[\cos(\pi t t) - cwt] \right]$
 $= \frac{V_{th}}{T} \left[-[-cost - cost] \right]$
 $= \frac{V_{th}}{T} \left[-[-cost - cost] \right]$

Single Phase full wave controlled rectifier-Center Tapped Configuration with RL-Load without Free wheeling Diode and Discontinuous Conduction Mode



Single Phase full wave controlled rectifier-Center Tapped Configuration with RL-Load without Free wheeling Diode and Discontinuous Conduction Mode



Single Phase full wave controlled rectifier-Center Tapped Configuration with RL-Load with Free wheeling Diode and Discontinuous Conduction Mode



Single Phase full wave controlled rectifier-Bridge Configuration with R-Load



Single Phase full wave controlled rectifier-Bridge Configuration with R-Load



Single Phase full wave controlled rectifier-Bridge Configuration with RL-Load without Free wheeling Diode and Discontinuous Conduction Mode



Single Phase full wave controlled rectifier-Bridge Configuration with RL-Load with Free wheeling Diode and Discontinuous Conduction Mode



Single Phase full wave controlled rectifier-Bridge Configuration with RL-Load without Free wheeling Diode and Continuous Conduction Mode









$$Fifteet of Source Sudationce in 1-de Fully
Cartualited bridge Redifier
(archield bridge Redifier will Cartinuums Conduction
$$V_0(m_{cw}) = \frac{1}{T} \int_{0}^{T} v_0(t) \cdot dust$$

$$(archield bridge Redifier in arcy, codput voltage,
(archield bridge Redifier in arcy, codput voltage,
$$\frac{1}{T} \int_{0}^{T} v_0(t) \cdot dust$$

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(archield bridge in arcy, codput voltage,
(archield bridge in arcy, codput voltage,
$$\frac{1}{T} \int_{0}^{T} v_0(t) \cdot dust$$

$$= \frac{1}{T} \int_{0}^{T} v_0(t) \cdot dust$$

$$= \frac{2V_{10}}{T} (cost - \frac{V_{10}}{T} (cost - \frac{V_{10}}{$$$$$$$$



Three Phase Voltages



Three Phase Voltages



Three Phase Voltages



















Three phase Rectifiers
R-Load:-
(For 30° < d < 150°)
RMS load Voltage,
Vo(mms) =
$$\left[\frac{1}{(2\pi/3)}\int_{30+2}^{120} V_{14} \frac{5\pi - 3\lambda}{3\pi} + \frac{5in(2\lambda + \pi)}{\pi}\right]^{1/2}$$

Three Phase Half wave Controlled Rectifier with RL Load



Three Phase Half wave Controlled Rectifier with RL Load



Three Phase Half wave Controlled Rectifier with RL Load



Three Phase Half wave Controlled Rectifier with RL Load with Free Wheeling Diode



Common Cathode Configuration



Three Phase Voltages





Three Phase Fully Uncontrolled Bridge Rectifier



Three Phase Fully Controlled Bridge Rectifier with R-Load






Three phase Rettins
R-load:-
Continuous Conductive Mode (
$$d < 60^{\circ}$$
)
Avery: load voltage.
 $v_0(av_1) = \frac{1}{T} \int^{T} v_0(d) \cdot dust$
 $= \frac{1}{2\pi} \int^{T} v_0(d) \cdot dust$

 $= \frac$

Three place Retting
R-load:
Continuous Gudueting Mode (
$$d < 60^{\circ}$$
)

$$= \sqrt{0} (Gay) = \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\cos \left(\frac{\pi}{3} + L \right) - \cos \left(\frac{2\pi}{3} + L \right) \right]$$

$$= \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\left[\cos \frac{\pi}{3} \cos d - \sin \frac{\pi}{3} \sin d \right] - \left[\cos \frac{2\pi}{3} \cos d - \sin \frac{\pi}{3} \sin d \right] \right]$$

$$= \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\left[\left(\frac{1}{2} \right) \cos d - \left[-\frac{\sqrt{12}}{2} \right] \sin d \right] - \left[\left(-\frac{1}{2} \right) \cos d - \left[-\frac{\sqrt{12}}{2} \right] \sin d \right] \right] \right]$$

$$= \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\left[\left(\frac{1}{2} \right) \cos d - \left[-\frac{\sqrt{12}}{2} \right] \sin d \right] \right]$$

$$= \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\frac{1}{2} \left(\cos d - \frac{\sqrt{12}}{2} \right) \sin d + \frac{1}{2} \left(\cos d + \frac{\sqrt{12}}{2} \right) \sin d \right]$$

$$= \frac{3 \sqrt{3} \sqrt{10}}{\pi} \left[\frac{1}{2} \left(\cos d - \frac{\sqrt{12}}{2} \right) \sin d + \frac{1}{2} \left(\cos d + \frac{\sqrt{12}}{2} \right) \sin d \right]$$

Three plane Richtins
R-Load :-
continuous Conduction Mode (d <60°)
Vo (Run)) =
$$\int \frac{1}{T} \int^{T} V_{0}^{*}(t) \cdot dust$$

 $= \left(\frac{1}{(\frac{2\pi}{c})} \int_{30+4}^{0} V_{0}^{*}(t) \cdot dust\right)^{V_{c}}$
 $= \left(\frac{3}{R} \int_{(\sqrt{3} V_{00} Sin}^{0} (\omega t + 30)) \int_{dust}^{V_{c}} V_{c}$
 $= \left(\frac{9 V_{0}^{*}}{2R} \left(\int_{30+4}^{0} 1 - (0)2 (\omega t + 30)\right) \right)^{V_{c}}$

Three phase Rechting
R-Load :-
Centiumers Conduction Mede
$$(d < 66^{\circ})$$

 $= V_0(3um) = \left(\frac{qv_{nn}^{\circ}}{3\pi} \left[60 - \frac{1}{2} \left[5in 2 \left(90 + 1 + 30\right)\right]^{1/2} - 5in 2 \left(30 + 4 + 30\right)\right]^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{3\pi} \left[\frac{\pi}{3} - \frac{1}{2} \left[5in \left(240 + 24\right) - 5in \left(120 + 24\right)\right]\right]^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{3\pi} \left[\frac{\pi}{3} - \frac{1}{2} \left[5in 240 \cos 24 + \cos 25in 24 - (512n 55in 24)\right]\right]^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{2\pi} \left[\frac{\pi}{3} - \frac{1}{2} \left[-\frac{43}{2} \cos 24 + \cos 25in 24 + (512n 55in 24)\right]\right]^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{2\pi} \left(\frac{\pi}{3} - \frac{1}{2} \left[-\frac{43}{2} \cos 24 - \frac{1}{2} 5iy 64 + \frac{1}{2} 5iy 64\right]\right)^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{3\pi} \left(\frac{\pi}{3} + \frac{1}{2} \sqrt{3} \cos 24\right)^{1/2}$
 $= \left(\frac{qv_{nn}^{\circ}}{3\pi} \left(\frac{\pi}{3} + \frac{1}{2} \sqrt{3} \cos 24\right)^{1/2}$

Three place Rutifies
R-load:-
D:s(continuous) Conductions Mode (
$$d > 60^{\circ}$$
)
Average load voltage.
 $V_0(av_3) = \frac{1}{T} \int^{T} V_0(4) \cdot dust$
 $= \frac{1}{(\frac{1}{2T})} \int_{T/c}^{TT/c} V_{ab}(4) \cdot dust$
 $= \frac{c}{2T} \int_{T/c+L}^{TT/c} \int_{T}^{TT/c} V_{ab}(4) \cdot dust$
 $= \frac{c}{2T} \int_{T/c+L}^{TT/c} \int_{T}^{TT/c} \int_{T/c}^{TT/c} \int$





Three phase Rechting
R-Load :-
Discriticular Conduction Mode (
$$d > 60^{\circ}$$
)
= $V_0(\pi lood) = \left(\frac{qV_{loo}^{\prime\prime}}{\pi} \left((150-30-4) - \frac{1}{5}\left(5in 2\left(150+30\right) - \frac{1}{5}\right)\right)^{V_{a}}$
= $\left(\frac{qV_{b}^{\prime\prime}}{\pi} \left(120-4 - \frac{1}{5}\left(5in 360 - 5in \left(120+24\right)\right)\right)^{V_{a}}$
= $\left(\frac{qV_{b}^{\prime\prime}}{\pi} \left(\frac{2\pi}{3} - 4 + \frac{1}{5} 5in \left(120+24\right)\right)^{V_{a}}$
= $\left(\frac{qV_{b}^{\prime\prime}}{\pi} \left(\frac{2\pi}{3} - 4 + \frac{1}{5} 5in \left(120+24\right)\right)^{V_{a}}$



Three phase Retifiers
RL-load :-
Average load voltage.
Vo (avg) =
$$\frac{1}{T} \int^{T} V_0(t) \cdot dust$$

 $= \frac{1}{T} \int^{T} V_0(t) \cdot dust$
 $= \frac{1}{T} \int^{T} V_0(t) \cdot dust$

These place Reduins

$$R_{L-load} := \frac{1}{V_{0}(y_{min})} = \sqrt{\frac{1}{T}} \int_{0}^{T} V_{0}(t) \cdot dust} = \left(\frac{1}{T} \int_{0}^{T} V_{0}(t) \cdot dust}{\left(\frac{1}{2\pi}\right)^{\frac{1}{2}} V_{0}(t) \cdot dust} \int_{0}^{t} U_{0}(t) \cdot dust} \int_{0}^{t} U_{0}(y_{0}) = \frac{1}{2\pi} \left(\frac{1}{2\pi} \int_{0}^{10+4} 3 V_{0}(y_{0}) \cdot (u_{0}t + 130)}{1 - (c_{0}) 2 (u_{0}t + 130)} \int_{0}^{t} U_{0}(y_{0}) = \frac{3V_{0n}}{2} \left(\frac{2\pi}{3} + \frac{d_{3}}{\pi} \cos 2x\right)^{\frac{1}{2}} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} - \frac{(o_{1} - (o_{2}) 2 (u_{0}t + 130))}{2}\right)^{\frac{1}{2}} \int_{0}^{10} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} (1 - (o_{1} - 1)) - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} (1 - (o_{1} - 1)) - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} (1 - (o_{1} - 1)) - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \left(\frac{1}{2\pi} + \frac{1}{2\pi} - \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{1}{2\pi} + \frac{1}{2\pi} \int_{0}^{10+4} \frac{1}{2\pi} + \frac{$$

Three phase Rectilius 3-0 Semicarrates :-1.(+) ve(t) 0 06 04















$$\frac{1}{2\pi} \sum_{i=1}^{2} \frac{1}{2\pi} \sum_{i=1}^{2} \frac{1}{2\pi$$

Three phase Retilies
3-9 Semiconvertion :-

$$\chi \ge 60^{\circ}$$
,
 $V_0(av_3) = \frac{1}{(\frac{2\pi}{3})} \left(\int_{30+4}^{210} V_{ac}(4) \cdot dwt \right)$
 $= \frac{3}{2\pi} \left(\int_{30+4}^{210} \sqrt{3} V_{ac} Sin(cdt - 30^{\circ}) dwt \right)$
 $= \frac{3\sqrt{3} V_{ba}}{2\pi} \left(-cos(wt - 30) \right)^{210}$
 $= \frac{3\sqrt{3} V_{ba}}{2\pi} \left(-\left[cos(210 - 30) - \frac{3\sqrt{3} V_{ba}}{2\pi} \left[-\left(cos(100) - cos(2) \right) \right] \right)$
 $= \frac{V_0(av_3)}{2\pi} = \frac{3\sqrt{3} V_{ba}}{2\pi} \left[1 + cos(2) \right]$

$$Three phase Restitions$$

$$3-9 Semi Gauniton :=$$

$$VO(2nm) = \left(\frac{9v_{m}}{4\pi} \left((60-4) - \frac{1}{2} \left(5in 2u_{0} - 5in 2 \left(6n42 \right) \right) + \left(60 + 4 \right) - \frac{1}{2} \left(5in 2 \left(120 + 4 \right) \right) \right)^{1/2} + \left(60 + 4 \right) - \frac{1}{2} \left(5in 2 \left(120 + 24 \right) \right) \right)^{1/2} + \frac{1}{2} \left(5in \left(120 + 24 \right) - \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in \left(2u_{0} + 24 \right) - \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in \left(2u_{0} + 24 \right) - \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in \left(2u_{0} + 24 \right) - \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{5}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \right)^{1/2} + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \left(5in 120 \left(2u_{0} + 4 \right) + \frac{1}{2} \left(5in 120 \left$$

$$Three phase Reditions$$

$$3-9 Seemi (annulten :-
= Vo(nun)) = \left(\frac{9V_{u}}{4\pi}\left(120 + \frac{\sqrt{3}}{2} + \frac{1}{2}\left(\frac{\sqrt{3}}{2} \cos 2\lambda - \frac{1}{2} \sin 2\lambda\right)\right)^{1/2}$$

$$= \left(\frac{9V_{u}}{4\pi}\left(120 + \frac{\sqrt{3}}{2} + \frac{\sqrt{3}}{2} \cos 2\lambda - \frac{1}{4} \sin 2\lambda\right)\right)^{1/2}$$

$$= \left(\frac{9V_{u}}{4\pi}\left(120 + \frac{\sqrt{3}}{2} + \frac{\sqrt{3}}{4} \cos 2\lambda - \frac{1}{4} \sin 2\lambda\right)\right)^{1/2}$$

$$= \left(\frac{9V_{u}}{4\pi}\left(\frac{2\pi}{3} + \frac{\sqrt{3}}{2} + \frac{\sqrt{3}}{2} \cos 2\lambda\right)\right)^{1/2}$$

$$= \left(\frac{9V_{u}}{4\pi}\left(\frac{2\pi}{3} + \frac{\sqrt{3}}{2} + \frac{\sqrt{3}}{2} \cos 2\lambda\right)\right)^{1/2}$$

Three phase Reditions
3-9 Semi Gunuten :-

$$\ll \geq 6^{\circ}$$
, $\forall \circ (\pi u_{1}) = \sqrt{\frac{1}{(\frac{2\pi}{3})} \left(\int_{30+4}^{210} \sqrt{\alpha_{c}} (4) \cdot dus \right)} \\
= \left(\frac{2}{2\pi} \left(\int_{30+4}^{10} [\sqrt{3} \sqrt{u_{0}} 5^{2} u (ut - 30)] \cdot dus \right)^{1/4} \\
= \left(\frac{9 \sqrt{u_{1}}}{2\pi} \left(\int_{30+4}^{10} (c_{0}) 2 (ut - 30) \right) \right)^{1/4} \\
= \left(\frac{9 \sqrt{u_{1}}}{4\pi} \left((210 - 30 - 4) - \frac{1}{2} \left(5 \ln 2 (210 - 30) - 5 \ln 2 (30 + 4 - 30) \right) \right)^{1/4} \right)^{1/4}$



Thrue phone Retifies
() A 3-\$\varphi\$ hells-wave certaches specifies has a supply
as 220×1ph. Detunde the average loas voltage
for faing angles \$\vec{a}\$ 0°, 30°, 60° assuming
a trypsister voltage drop \$\vec{a}\$ 1.5\$ and Centimens
load leanest.

$$V_0(av_1) = \frac{3\sqrt{3}V_{m}}{2\pi} (cost - Down across
= \frac{3\sqrt{3}}{2\pi} \times 200 \int 2 \times (cost - 1.5)$$

$$d = 0°, V_0(av_3) = \frac{3\sqrt{3}}{2\pi} \times 200 \int 2 \times (cost - 1.5)$$

$$= \frac{3\sqrt{3} \times 200 \int 2 \times (cost - 1.5)}{2\pi\pi}$$

$$= \frac{3\sqrt{3} \times 200 \int 2 \times (cost - 1.5)}{2\pi\pi}$$

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$$= \frac{3\sqrt{3} \times 200 \int 2 \times (cost - 1.5)}{2\pi\pi}$$









O A 1-0 diode bridge nedfier Supplies a highly suduelive load. The load Cungent can be assured to be stipple free. The ac supply Side current wareforms will be -(a) Since soid (b) Constant de (b) Square (d) Triangular




(Ju a 1-0 diede bridge mitifier Shown below, the load newstance is son. Vs (4) = 200 sin (w+), Where w = 2xx 50 Mad/sec. The power dissipates in the load nu:stance "R" is -(c) 3200 (b) 400 (c) 400 (d) 800

$$P_{0} = \int_{0}^{\infty} (n\omega_{1}) \cdot R$$

$$(q)$$

$$P_{0} = \frac{V_{0}^{*}(n\omega_{2})}{R}$$

$$V(f) = 200 \text{ Sin (}\omega_{1})$$

$$(\omega_{1}) \cdot \omega_{2} = 2\pi \times 50 \text{ Jumb} \text{ Jee}$$

$$R = 500 \text{ Image in the set of the set$$

P = Vnus · Inus · cosø Vnus = RMS Source voltege Inus = RMS Source Connect P = Vnus. Irnus. coso, Vnus = RMS Sounce voltage Ilmus) = RMS value of fundementer cument (0) \$1 = Augle Setween Vrus and Inprus) Cos \$ = Super pour factor (1) supply pour factor (a) \$1 = Fernel curent-1 Super PF (A) Fundements Supply PF

$$P = V_{y_{1}u_{1}} \cdot \overline{I}_{y_{1}u_{1}} \cdot co) \phi \rightarrow O$$

$$P = V_{y_{1}u_{1}} \cdot \overline{I}_{y_{1}u_{1}} \cdot co) \phi \rightarrow O$$

$$O = O \cdot V_{y_{1}u_{1}} \cdot \overline{I}_{y_{1}u_{1}} \cdot co) \phi = V_{y_{1}u_{1}} \cdot \overline{I}_{y_{1}u_{1}} \cdot cos \phi$$

$$= \sum cos \phi = \sum 1 (y_{1}u_{1}) \cdot cos \phi$$

$$\overline{I}_{y_{1}u_{1}} \cdot cos \phi$$

$$= \sum J_{u_{1}u_{1}} pres pr = \frac{T_{1}(y_{1}u_{1})}{T_{y_{1}u_{1}}} \cdot \frac{r_{u_{1}u_{1}}}{r_{u_{1}u_{1}}} pres$$

$$= \sum J_{u_{1}u_{1}} pres pr = COF \times FDF$$

$$\begin{split} \widehat{I}_{1}(\eta_{u}u_{1}) &= \frac{10\sqrt{L}}{\sqrt{L}} = 10 \text{ Amp} \\ \widehat{I}_{\eta_{u}}(u_{1}) &= \sqrt{\sum_{1}^{v}(\eta_{u}u_{1}) + \sum_{3}^{v}(\eta_{u}u_{1}) + \sum_{3}^{v}(\eta_{u}u_{1})} \\ &= \sqrt{\sum_{1}^{v}(\eta_{u}u_{1}) + \sum_{3}^{v}(\eta_{u}u_{1}) + \sum_{3}^{v}(\eta_{u}u_{1})} \\ &= \sqrt{\sum_{1}^{v}(\eta_{u}u_{1}) + \sum_{1}^{v}(\eta_{u}u_{1}) + \sum_{1}^{v}(\eta_{u}u_{1})} \\ &= \sqrt{\sum_{1}^{v}(\eta_{u}u_{1}) + \sum_{1}^{v}(\eta_{u}u_{1}) +$$



$$s_{1} = \frac{1}{2} + \frac{1}{2$$



- PIV or PRV = $2*V_m$; Center Tapped Rectifier = 2*50*sqrt(2)
 - = 100 * sqrt(2).

(a) 90° (b) 180°
(c) 270° (d) 360° 3°

$$V_{1}(t) \bigoplus_{j=1}^{n} \frac{1}{j} \frac{1}{t} \frac{1}{t}$$



Vs = 100 Sim (wt), w = 1007 mad/sec. L = 31.83 with . The Juiked annest in "L" " teno. Shitch is closes at 1: 2.5 mile. The Peak Value & Juduits Curet in first Gdis.

$$t \ge 0,$$

$$i_{L} = \frac{V_{u_{1}}}{\omega_{L}} \left[1 - \cos(\omega) \right]$$

$$t \ge \frac{V_{u_{1}}}{\omega_{L}} \int_{0}^{t} \sin(\omega) \cdot dt$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \int_{0}^{t} \sin(\omega) \cdot dt$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \left[-\cos(\omega) \right]_{1-\tau_{T_{1}}}^{t}$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \left[(0) \left(2 \cdot \frac{\tau_{T_{1}}}{\tau_{1}} \right) - (0) \omega^{t} \right]$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \left[(0) \left(100\overline{\Lambda} \times \frac{2 \cdot \tau_{T_{1}}}{\tau_{1000}} \right) - (0) \omega^{t} \right]$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \left[(0) \left(100\overline{\Lambda} \times \frac{2 \cdot \tau_{T_{1}}}{\tau_{1000}} \right) - (0) \omega^{t} \right]$$

$$= \frac{V_{u_{1}}}{\omega_{L}} \left[(0) \left(100\overline{\Lambda} \times \frac{2 \cdot \tau_{T_{1}}}{\tau_{1000}} \right) - (0) \omega^{t} \right]$$



[dithrout L_s,

$$V_0 = \frac{2V_{un}}{\pi} cost : contailles Rethlin
 $V_0 = \frac{2V_{un}}{\pi} : uncertailles Rethlin
Litht Ls,
 $V_0 = \frac{V_{un}}{\pi} [cost + (os(litha))] contailles
(a) $V_0 = \frac{2V_{un}}{\pi} cost - 4fls Io$
 $V_0 = \frac{V_{un}}{\pi} [1 + (osM]] contailles
 $V_0 = \frac{2V_{un}}{\pi} [1 + (osM]] contailles
Rettlin
 $V_0 = \frac{2V_{un}}{\pi} - 4fls Io$$$$$$$

$$V_{0} = \frac{2 V_{u}}{\pi} - \frac{4 f L_{3} I_{0}}{\pi}$$

$$= \frac{2 \chi 220 d 2}{3.19} - \frac{4 \chi 50 \times 10 \times 10^{-3} \times 19}{3.19}$$

$$= 189.07 \times \frac{1}{\pi} \left[1 + \frac{10 M}{3} \right]$$

$$= \chi 189.07 = \frac{220 d 2}{3.19} \left[1 + \frac{10 M}{3.19} \right]$$

$$= \chi 189.07 = \frac{220 d 2}{3.19} \left[1 + \frac{10 M}{220 d 2} \right]$$

$$= \chi 180 + 49.2$$

$$= 22 4.2^{\circ}.$$



$$V_{0} = \frac{2V_{un}}{\pi} \cos \lambda ; d \neq 0$$

$$V_{0} = \frac{2V_{un}}{\pi} ; d = 0$$

$$At d = 0^{\circ}, V_{0} = 300v$$

$$\Rightarrow 300 = \frac{2V_{un}}{\pi} \rightarrow 0$$

$$At d = 60^{\circ},$$

$$V_{0} = \frac{2V_{un}}{\pi} \cos \lambda$$

$$= 300 \cos (60^{\circ})$$

$$= 300 \times \frac{1}{2}$$

$$\Rightarrow V_{0} = 150v$$

10) The thyruston in the figure is fined at an angle of "d" in every the half cycle of the Supert Voltage . 32 the peak value of the Instantoning output voltage quals 2300, then the finity angle "I "is close to -(c) 90° (4) 83.6 (1111) (c) 90° (4) 83.6 (1111) (0) 13









$$V_{0} = \frac{V_{un}}{2\pi} \left[1 + \cos L \right]$$

=> $70 = \frac{32s}{2x^{3}} \left[1 + \cos L \right]$
=> $1 + \cos L = 1 \cdot 3526$
=> $\cos L = 1 \cdot 3526 - 1 = 0 \cdot 3526$
=> $d = 69 \cdot 35^{\circ}$.

A fully Controlles Governotes bridg. Judy a hypely Juduelar least with nipple free load current. The Supert Supply (V;) to the bridge is a Simusoider source. d = 30°. The Super- pane facts 2 the bridge is -

$$\begin{array}{l} \text{Juput PF = COF \times FOF} \\ = \frac{24}{\pi} \times (\omega \phi_{1}) \\ = 0.9 \times (0Jd) \\ = 0.9 \times (0J (60^{\circ})) \\ = 0.77794^{\circ} \\ \end{array}$$

$$\begin{array}{l} \overset{\circ}{}_{J_{1}} (4) = \sum_{\eta \leq J_{1}} \frac{4 \cdot \mathcal{I}_{0} \cdot \sin(\eta \omega t - \eta d)}{\eta \wedge} \\ (\omega \phi) = \frac{\mathcal{I}_{J_{1}}}{\mathcal{I}_{J_{2}}} \cdot \cos(\phi_{1}) ; \phi_{1} = -\eta d \\ \hline \mathcal{I}_{J_{1}} (4) = \sum_{\eta \leq J_{1}} \frac{4 \cdot \mathcal{I}_{0}}{\eta \wedge} \cdot \sin(\eta \omega t - \eta d) \\ \hline \mathcal{I}_{0} (\omega \phi) = \frac{\mathcal{I}_{J_{1}}}{\mathcal{I}_{J_{2}}} \cdot \cos(\phi_{1}) ; \phi_{1} = -\eta d \\ = \frac{4}{\eta \leq d_{1}} \frac{\eta}{\eta \wedge} \frac{\eta}{d_{1}} \cdot \cos(\phi_{1}) \\ = \frac{4}{\eta \leq d_{1}} \frac{\eta}{\eta \wedge} \frac{\eta}{d_{1}} \cdot \cos(\phi_{1}) \\ = \frac{24}{\eta \leq d_{1}} \cos(\phi_{$$

A 1-Q fully - Controlled to youiston Convertion is used to abtain an average voltage of 1800 with-10 Aug Constant Curryent 15 feed a de load. It is feed forem 1-Q Ac Supply of 2300, 50 Hz. Neylect the Same Impedance. The power factor (ground offer to two decimal places) of Ac mainy is _____

Var = Vo = 1800, Vs = 2300 For a 1-9 fully Controlles topoth, Vo = 2 Vus cosd => 180 = 27.230 /2 cosd => Cold = 0.869 Supret PF = 252 cosd = 0.9 (osd = 0.9 × 0.869 = 0.786 lag

(6) A 1-Ø, full-bridge diode metifica fed from a 2300, Sotts Simsoidal surger supplies a series Cambinders of finite mistance," R", and a very large Inductance," L". The two most dominant forquery components in the Source compart are

> (a) SOH3, OH3 (b) SOH3, 100h3 (c) SOH3, 150H3 (d) 150H3, 250H3

 $I_{sn}(+) = \underbrace{T}_{n \ge 1,3,s}, \underbrace{4I_0}_{n \ge n} Sin(n \ge 1, -n \ge 1)$ for a died bridge methin, d = 0 > In (+) = I 4 Io Sin(nw+) 7=1.3, 1.- 77 Dominant frequencies _ n=1 => 410 n=3 -) 450 N=5 -) 450 JA N=5 -) 430 58 => f=sotis, 3x50, 5x50 => 50H3, 150H3, 250H3

(17) A 1-9, full bridge, fully Controlka typistor nutifier feeds a load Camponising a los mistance in Suries with a very lary. Inductance. The nectifica is fed forous an Ideal 2300, SOH3 Sinusoided Source though Cashes which have nyligish Internet noistance and a total Inductance of 2.28 with . If the thy nistar are trijjered at an angle & = 45°, the Commutation overlap angle Indepare (nound off to 2 decimal place) : -

$$\begin{split} & \text{With ``L_{J}`',} \\ & \text{V}_{0} = \frac{2 \text{Vm}}{\pi} (\text{cs} \text{d} - 4 \text{fL}_{S} \hat{\text{I}}_{0} \\ & \Rightarrow \hat{\text{I}}_{0} \text{KR} = \frac{2 \text{Wm}}{\pi} (\text{cs} \text{d} - 4 \text{fL}_{S} \hat{\text{I}}_{0} \\ & \Rightarrow \hat{\text{I}}_{0} \text{KR} = \frac{2 \text{Wm}}{\pi} (\text{cs} \text{d} - 4 \text{fL}_{S} \hat{\text{I}}_{0} \\ & \Rightarrow \hat{\text{I}}_{0} \text{KR} = \frac{2 \text{X} 2 \text{Je} \sqrt{2 \times (\text{cs} \text{H}_{S})}}{\pi} \\ & \text{H}_{X} \text{So} \text{X} 2 \cdot 2 \text{Je} \text{X} \hat{\text{Ie}}^{3} \text{R}_{0} \\ & \Rightarrow \hat{\text{I}}_{0} = 14 \text{Aunp} \\ & \text{Voltay. drep du le ``L_{S}`',} \\ & 2 \text{WL}_{S} \hat{\text{I}}_{0} = \text{Vm} \left[(\text{cs} \text{d} - (\text{cs} (\text{d} + \text{M})) \right] \\ & \Rightarrow 14 = \frac{2 3 \text{o} \sqrt{2}}{2 \times (\text{ce} \text{A} \times 2 \cdot 2 \text{Je} \times \text{i})^{3}} \\ & \Rightarrow M = 4 \cdot 8^{\circ} \\ & = 4 \cdot 8^{\circ} \end{split}$$

8 A phase Gatallus 1-\$ putition,
Supplies by an Ac Saure, field
pairs to an RLE load as sham
in the friene. The putities output
voltage has an average value
given
$$V_0 = \frac{V_{10}}{2\pi} (3 + (0.14), where $\frac{1}{2\pi}$ and "d" is the
finity angle. Of the parent
delaceus to the lossless betters
is 1600 W, "d" in degree is -
(up to 2 deciment places)$$

Pawer transforms to the sev better,

$$E \cdot I_0 = 1600$$

 $\Rightarrow I_0 = \frac{1600}{80} = 20 \text{ Amp}$
 $V_0 = I_0R + E = 20x2 + 80$
 $= 120V$
from the sum,
 $V_0 = \frac{V_{un}}{2\pi} (3 + (054), V_{un} = 80R)$
 $\Rightarrow 120 = \frac{80R}{2\pi} (3 + (054))$
 $\Rightarrow cosd = \frac{120}{40} - 3 = 0$
 $\Rightarrow d = (05^{-1} (0))$
 $\Rightarrow d = 90^{-1}$





	× 1, 0	t Soc vo	Vo	, , , , , , , , , , , ,	5 Vb		22 wł d=30°
				× ,	~ 747	en ente	3 WF
L	wt	т,	T _k	Dı	01	Vo	3 * UF
L	wt ocwtee	T, OFF	TL ON	D ₁ ON	D ₂ OFF	Vo O(NFA)	3 * ω
L	wt Ocwtel dewtek	T, OFF ON	TL ON OFF	D ₁ ON ON	D ₁ OFF OFF	Vo O(VFA) Vab	γ
L	Wt Ocwtel dewtek Kewtek	T, OFF ON ON	TL ON OFF OFF	Di ON ON OFF	D2 OFF OFF ON	νο Ο(VFA) <u>ναь</u> Ο(VFA)	γ . ωτ
1-9 Seeni Converters

20 A half - Controlled 1-9 diode bridge netitien is supplying an RL-load. It is operated at a finity angle "2" and the load current is Continuous. The finaction 2 cycle that the free wheeling diode Conducts is

し)(」) (a) ! (d) <u>x</u> (c) d 28



1-9 Seeni Converters

(2) The figure below shars the Cinemitdiapain of a Controlled Auctilier Supplied forom a 230 v, 50 Hz, I-& Voltage Sourgee and a 10:1 Ideal toransformer. Assume that all devices age Ideal. The firing angles of the thyristory T, and Tz age 90° and 270° Augestudy

The RMS value of the current through diode "D;" in aupens is -





For a Resistive load, the RMS current through the Free wheeling diode is **0** Amp.

1-9 Seeni Converters

(22) In the gives nectifier, the delay angle & the thynister "T," meaned from the posiler going zero (nowing of vs is 30°. If the Input voltage Vs is 100 Sin (100 The)v, the average voltage across "R" (in volt) under Steady-state; -





1-9 Seeni Converters

(23) The waveform of the current drawn by a Suni-Conster from a Simusoides de voltage Sance is shawn in the fyure . It Is = 20 Aug, the news vake of fundamental Component of the current is - Amp (upto 2 decimal places) VI Vusner

IJu(+) = 2 4 Io cos(nx). n=1,3,5.. MR Sin (nwt - nx) II(アルリ) = 4Io × (の)(土) = 4×20 × cos(15") 7.12 = 17.39 Amp

() A 3-9 diade bridge nection is feeding a Contant de current of 100 Amp to a highly Juduelie load. J. 3-0, 415V, SOH3 ac Source is supplying to this bridge Julificer, then the guns value of the Cangent in each died in aupen is -

p

5

-



Three Phase Fully Uncontrolled Bridge Rectifier



- Each SCR conducts for 120 degree
- Each SCR pair conducts for 60 degree
- Each Phase conducts for 240 degree
- Average value of Thyristor current, $I_0(Avg)$ =Peak value*(120/360)= I_0/3 Amperes
- RMS value of Thyristor current, Io(rms)=Peak value*Sqrt(120/360)=Io/sqrt(3) Amperes
- Average value of Source current, Is(Avg)= 0 Amperes
- RMS value of Source current, Is(rms)=Io*sqrt(120/180)=Io*sqrt(2/3) Amperes



• Average value of Thyristor current, **Io**(**Avg**)=Peak value*(120/360)

= **Io/3 Amp** = 100/3 = 33.33 **Amp**

• RMS value of Thyristor current, **I**₀(**rms**)=Peak value*Sqrt (120/360)

=Io/√3 Amp

 $= 100/\sqrt{3} = 57.73$ Amp



• Average value of Source current, Is(Avg)= 0 Amp

The wave is a symmetrical waveform. The average value will be equal to Zero.

• RMS value of Source current, $I_s(rms) = I_0 \sqrt[*]{(120/180)}$

= Io* $\sqrt{(2/3)}$ Amp

 $= 100 * \sqrt{(2/3)}$ Amp.

(2) A 3-0 fully Cartaolles thynistor bridge Greater is used as line Committated Inveter to feel soke pases, 4200 de to a 3-0, 415v (lim), so his ac mainy. Cansider de link cunnent lo be Castant, The gruns Current 2 the thyrista is -

```
P output = 50 \text{ kw} = 50*1000 \text{ watts}

Output voltage = 420 \text{ v}

P_0 = V_0 * I_0

50 * 1000 = 420 * I_0

I_0 = (50*1000)/420 = 119.05 \text{ Amps}
```

 $I_{T(rms)} = I_0 * (1/sqrt(3)) = (119.05)/1.732 = 68.73$ Amps.

 $I_{T(Avg)} = I_0/3 = 119.05/3 = 39.68$ Amps

$$I_0 = (50*1000)/420 = 119.05$$
 Amps

 $I_{T(rms)} = I_0 * (1/sqrt(3)) = (119.05)/1.732 = 68.73$ Amps.

 $I_{T(Avg)} = I_0/3 = 119.05/3 = 39.68$ Amps

 $I_s(Avg) = 0$ Amps

 $Is(rms) = I_0 * sqrt(2/3) = 119.05 * (1.414/1.732) = 97.19$ Amps

For a Three Phase fully controlled Thyristor bridge converter (R, RL/RLE load) with continuous conduction and constant current,



In the given sum, it is a line commutated inverter

i.e., $\alpha = 90^{\circ}$ and Vo = Negative = -420 V





$$i_{sn}(t) = \sum_{n=1,3,5..} \frac{4 * I_o}{n\pi} * \sin(\frac{n\pi}{3}) * \sin(nwt - n\alpha)....(1)$$

$$i_{sn}(t) = \sum_{n=1,3,5..} \frac{4 * I_o}{n\pi} * \sin(\frac{n\pi}{3}) * \sin(nwt)....(2)$$

Equation (1) represents the fourier series representation for a controlled rectifier and Equation(2) represents the fourier series representation for a Unontrolled rectifier



Isr1= Fundemental RMS source current



$$=I_{sr1} = \frac{\sqrt{6}}{\pi} * I_o Amp = \frac{\sqrt{6}}{\pi} * 119.05 = \frac{2.4494}{3.14} * 119.05 = 92.82Amp$$

Current Distortion Factor,

$$CDF = \frac{I_{sr1}}{I_{sr}}$$
where
$$I_{sr} = I_{s(rms)} = I_o * \sqrt{\frac{2}{3}} = 119.05 * \sqrt{\frac{2}{3}} = 97.19Amp$$

$$CDF = \frac{92.82}{97.19} = 0.95$$

-

Distortion Factor,

$$DF = \cos \alpha$$
$$= DF = \cos(-138.5)$$
$$= 0.7489$$

Input Power Factor or Supply Power Factor = CDF*DF

= 0.95 * 0.7489

= **0.7114**

3 A 3-0 diede bridge metilien is fed from a 400 v news, Solis, 3-0 ac Sauger, if the load is purely mistaie, then the Peak Instantances output voltage is equal to -



$$V_{ab}(t) = V_{ml} * \sin(wt + 30)$$

(Refer waveforms)

The peak instantaneous output voltage can be obtained when $wt = 60^{\circ}$ Vml = Maximum line voltage

At wt = 60° ,

$$V_{ab}(t) = V_{ml} * \sin(60 + 30) = V_{ml} * \sin(90) = V_{ml}$$

$$V_{ml} = \sqrt{2} * V_{rms} = 400\sqrt{2}V$$





Number of pulses in a cycle = 3

The ripple frequency at the output = $f_0 = n^* f_s$

Where $f_s = 400 \text{ Hz}$

fo= 3*400= 1200 Hz.

Number of pulses in a cycle = 3 Output Time period, $T_0 = (2*pi)/n=(2*pi)/3$ Supply Time period, T=2*pi

 $T_{o}/T=1/3$ $T_{o}=T/3$

f=1/T $f_0=3/T=3*f_s=3*400=1200 Hz$
A fully - Controlled 3-90 bridge Converter is wilking from a 4150, some as supply. It is supplying Constant Current of 100 Aup at yoov to De load. Assume Large Inductive succothing and nefect overlap. The new value of the Acline Curryent in Aupens (yound of to two decimal place) =

(5)

 $J_{0} = 100 \text{ Amp}$ For a fully Cathelles 3-49 bridge Converter. $J_{s}(mu) = J_{0} \times \int_{\frac{2}{3}}^{\frac{2}{3}}$ $= 100 \times \int_{\frac{2}{3}}^{\frac{2}{3}}$ = 81.65 Amp 6 A Six pulse thyristor bridge nectificon is connected to a balanced 3-9, so his ac sample. Assuming that the de output Current of the nectifica is Constant, the lawest haquanic component in die Ac Super current is 6) 300ths 6> 250 His (c) 150 His (d) 100 His

 $f_{su}(t) = \underbrace{\sum}_{n \neq i, \overline{3}, \overline{s}} \frac{4Io}{n\overline{x}} \cdot \underbrace{Sin(n\overline{x})}_{Sin(mut-m2)}$ m=1 -> Fundement-1 $m = 3 \implies i_{sn}(+) = 0$ m=5 2) isu(+) have some Value Lawut harmonic Component in die AC Supert current is , 5x 50 = 250 H3



DC - De pour Convitus A de choppen is a stalie durier (switch) used to obtain variable de voltege from Applications :a source of Constant de Voltage. 1) Trolly Care chopper (2) Battery openeted vehicles 3 Traction - Motor control Ede (Control of de motors from a common bus (Gataol q Induction motor) (Marine hoists Fork light trucks 1 Mine haulers



Control Stratyin :-The avery value of the cutput Voltage, "Vo" can be controlled by Time Ratio Gatas (TRC) Periodie opening and classing of 1) Constant fryning System the Switches. The two types & Contas starty:05 (Variash fryneng for openting the switches on employed System in de choppers are () Time natio Cantao) (TRC) (Current limit Control (CLC)





















Step up Converter(CCM)



Step up Converter(CCM)



Step up Converter(CCM)

DC-DC Convertors

Ripple in the satput valley,
$$\Delta v_c \simeq \Delta v_0$$
:
During Tan, the Capacitan is delivering
Pare to the test load.
 $f_c = -f_0$ voin. D^T
 $\Rightarrow c \frac{dv_c}{dt} = -f_0 \Rightarrow \int dv_c = \int -\frac{f_0}{c} \cdot dt$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} \Rightarrow \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
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 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_1 \cdot 0}{2fL_c} = \frac{v_1 \cdot 0}{2fL_c}$
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 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_0}{2fL_c}$
 $\Rightarrow \frac{v_0}{R(t-0)} = \frac{v_0}{2fL_c}$

Step up Converter(DCM)



Buck-Boost Converter(CCM)



Buck-Boost Converter(CCM)



Buck-Boost Converter(DCM)





Buck Growton :-

It the boundary (indition of Continuous)
and discritionary Guduction,

$$I_{L}(unin) = 0$$

 $\Rightarrow I_{L}(av_{J}) - AI_{L} = 0$
 $\Rightarrow I_{0} = AI_{L} \Rightarrow \frac{V_{0}}{R} = \frac{V_{J}(1-0)DT}{2L}$
 $\Rightarrow \frac{DV_{J}}{R} = \frac{V_{J}(1-0)DT}{2L}$
 $\Rightarrow (1-0) = \frac{2L}{RT}$
 $\Rightarrow D = 1 - \frac{2L}{RT}$

(2) A chopper is employed to charge a battery as obeen in the foure. The charging current is sAmp, the duly natio is 0.2. The choppen Output voltage is also shawn in belaw forme. The peak to peak Ripple Current in the charging Current is -66V SOP -> 200-

Peak le peak mipple current, $DJL = V_{s}(1-D)DT$ L $= 60 \times (1-0.2)0.2 \times 121=3$ 20×10^{-3} = 0.48 Amp.

(2) A chapper is employed to chage a battery as them in the foure. The charging current is sAmp, the duly statio is 0.2. The chopper Output voltage is also shawn in belas forme. The peak to peak nipple Current in the charging Current is SA LEZOMA 60V SOPE 200-

(2) A chopper is employed to change a battery as obrain in the foure. The charging current is sAmp, the duly natio is 0.2. The choppen Output voltage is also shawn in below forme. The peak to peak Ripple Current in the charging Current is -SA LEZOMH SOV rop 200-1

An Questing :-
() Maximum possible Suducts

$$(unquet)$$
,
 $I_{L}(unex) = I_{L}(arg) + \Delta I_{L}$
 $= S + 0.45$
 $= S + 0.45$
 $= S \cdot 24$ Amp.
 $I_{L}(unin) = I_{L}(unin) - \Delta I_{L}$
 $= S - 0.48$
 $= 4.76$ Amp.

3 In the Circuit Shaw, an Ideal Switch"s " is operated at 100 kmg with a duty matio of so 1. Gives that Die is 1.6 Amp peak to peak and "To" is 5 Aup de, the peak Current in Switch "s" is ____

Extra Question :-

At which duby cycle; "D", Change in supple current is waximum?

(4) Fijure (1, Shows the Cincuit diapases of a chappest. The Switch "s" in the Ciquit in the four (i) is suitcus such that the voltage "Vo" across the diade has the waveshape as Shaw to the foure (ii). The capacitona "c" is laye so that the voltage across it is constant. If the Switch"s" and the diade "D" are Ideal, then the peak to peak sipple (in any) is the Suductor Current is

ICOV 0-15 0-2 + Cm 6.05 6-1 0 fipme (:) Peak to peak supple, $\Delta I_{L} = V_{s} (1-D) D \cdot T$ D = Ton = 0.05 = 0.5 6-1 1]L = 100 x (1-0.5) 0.5 x 0.1x103 17.10-3 = 100 × 0.5×0.5×0.1 = 2.5 Amp.

(3) A buck Converter fuding a variable nuistre load is shaw in the forme. The Switching frequency of the Switch"s" is 100 ktrs and the duty Intio is 600 0.6. The output Volty, Vo = 36 v. Assume that all the components ay. Ideal, and that the output voltage is gripple free. The vake of "R" (in oney) that will make the Inductor current (in) just

Continuous i ----

$$B = \frac{1}{RT}$$

$$B = \frac{2L}{RT}$$

$$B = \frac{2L}{RT}$$

$$B = \frac{2L}{(-D)}$$

Boost Convector

(In the circuit shaws in the form, the switch is opentes at a duly Gele of 0.5. A lay. capacitor is connected across the load. The Inducty current is assumed to be Continuous. The avery. volty. across the load and the array. Current through the diode will nupectively be IL= YAY

$$V_{0}(av_{1}) = \frac{V_{s}}{1-0} = \frac{20}{1-0} = \frac{20}{0}$$

$$= 40v.$$

$$J_{0} = Beost Governon,$$

$$Sw = 0N \Rightarrow D = 0 + 5 = 10 = 0$$

$$Sw = 0 + 5 \Rightarrow D = 0 + 5 = 10 = 0$$

$$Sw = 0 + 5 \Rightarrow D = 0 + 5 \Rightarrow 10 = 0$$

$$I_{0}(av_{1}) = \frac{P}{1} \times \frac{x - 0}{7}$$

$$= 4 \times (1 - 0)$$

$$= 4 \times (1 - 0)$$

$$= 4 \times 0.5$$

$$= 2 Auap$$

Boost Converter

(c) Extra Questions:
(c) Id (now) =
$$\overline{I}_{L}(\alpha \eta) \times \sqrt{\frac{T-\Omega \overline{I}}{T}}$$

= $4 \times \sqrt{1-\Omega}$
= $4 \times \sqrt{1-\Omega}$
= $4 \times \sqrt{0.7}$
= $2 \cdot 82 \cdot 8 \operatorname{Aunp}$
(c) Switch (wright, Isw Isw
 $\overline{I}_{Sw}(A\eta) = \overline{I}_{L}(\alpha \eta) \times \frac{\Omega \overline{I}}{T}$
= $4 \times 0.7 = 2 \operatorname{Aunp}^{0}$
 $\overline{I}_{Sw}(\pi u_{N}) = \overline{I}_{L}(\alpha \eta) \times \sqrt{\frac{\Omega \overline{I}}{T}}$
= $4 \times 0.7 = 2 \operatorname{Aunp}^{0}$

T

-

Boost Converter

(a)
$$\overline{I}_{s}(av_{s}) = \overline{I}_{L}(av_{s})$$

(Alwayn)
 $\Rightarrow \overline{I}_{s}(av_{s}) = \frac{\overline{I}_{o}}{1-D}$
 $\overline{I}_{o} = \frac{v_{o}}{R} ; v_{o} = \frac{v_{s}}{1-D} = \frac{12}{1-o.4}$
 $\Rightarrow v_{o} = 20v$
 $\overline{I}_{o} = \frac{20}{20} = 1 \text{ Amp}$
 $\Rightarrow \overline{I}_{s}(av_{s}) = \frac{1}{1-o.4} = \frac{1}{0.6}$
 $\Rightarrow \overline{I}_{s}(av_{s}) = \frac{1}{0.6} = \frac{5}{3} \text{ Amp}$

Boost Converter

(7) In the figure Alean below, the Chappen feeds a specific load from a battery source. Market "C," is switches at 250 kms, will: a duly setio of 0.4. All the elements of the circuit are annues 18 be Ident Source All Source All

> (a) The avery. Some current in Amps In Steady state is ______ (b) The peak to peak Source Current gripple in Augs is ______

$$\Delta I_{L} = \frac{V_{s} \cdot D}{fL}$$

- = 12×0.4 250×1000× 100×10-6
- = 0.192 Amp
Boost Converter

(*) Extra Questions:-
(*)
$$f_{L}(max) = f_{L}(any) + \frac{Af_{L}}{2}$$

 $= \frac{5}{3} + \frac{0 \cdot 192}{2}$
 $= 1 \cdot 762 Amp$
(*) $f_{L}(min) = f_{L}(any) - \frac{Af_{L}}{2}$
 $= \frac{5}{3} - \frac{0 \cdot 192}{2}$
 $= 1 \cdot 5706 Amp$
(*) $f_{D}(any) = f_{L}(any) \times (1-0)$
 $= 5/3 \times (1-0)$

(4)
$$f_{0}(\pi, m) = f_{1}(x, y) \times \sqrt{1-0}$$

 $= \frac{5}{3} \times \sqrt{1-0.4}$
 $= 1.29091 Amp$
(5) $f_{3}(x, y) = f_{1}(x, y) \times D$
 $= \frac{5}{3} \times 0.4$
 $= 0.66664 Amp$
(6) $f_{3}(\pi, m) = f_{1}(x, y) \times \sqrt{D}$
 $= \frac{5}{3} \times \sqrt{0.4}$
 $= \frac{5}{3} \times \sqrt{0.4}$
 $= 1.05367 Amp$

Boost Converter

Fot the Switching Converter Shaws, anum steady stale operation. Also anum that the Components ap. Ideal, the Inductor current is always positive and Continuous and Switching period is "Ts". If the Voltage "VL" is as shaws, the duty cycle of the Switch"s"



In a Boost Contra Sw-on, VL (m) = Vs >> VL (on) = Vs = 15 v SW-OFF, $\vee_L(\mathfrak{M}) = \vee_J - \vee_O = \vee_O = \vee_J - \vee_L(\mathfrak{m})$ => Vo = 15 - (-4r) = 60V $V_0 = V_s$ 1-D 1- D >> D = 0.75

Buck - Boost Griventer

(9) In the Circuit Shown, all the elements age Ideal and the Surter "s" is operated at lokes and 60%. duly Matio. The capacitance is lary. enargh so that the ripple across it is nyligible and at Steady Stali acquires a voltage as shaw. The peak current in auperes drawn from the sov de Sanja is 504

$$V_{0} = \frac{V_{s}O}{1-O}, \quad \tilde{L}(ay) = \frac{T_{0}}{1-O}$$

Source (unpert = Switch (unpert, T_{sw}
= Curryent through the
Judactor, T_{L}
 $T_{Sw}(peak) = \tilde{L}(max)$
 $\Rightarrow \tilde{L}(max) = \tilde{L}(max)$
 $\Rightarrow \tilde{L}(max) = \tilde{L}(ay) + \Delta \frac{T_{L}}{2}$
 $V_{0} = \frac{V_{s}O}{1-O} = \frac{S0 \times 0.6}{1-O \cdot 6} = 75V$
 $T_{0} = \frac{V_{0}}{R} = \frac{7T}{1-O} = 15$ Amp
 $\tilde{L}(ay) = \frac{T_{0}}{1-O} = \frac{15}{1-O \cdot 6} = 37.5$ Amp
 $\Delta T_{L} = \frac{V_{s}O}{7L} = \frac{S0 \times 0.6}{10 \times 1000 \times 0.6} = 5$ Amp
 $T_{sw}(peak) = T_{L}(max) = 37.5 + 5/L$
 $= 40$ Amp

Buck - Boost Griter

(The Supert Voltage Vac of the buck-boost Convertig shaw below varies from 322 to 72v. Anum that all components are Ideal, Inductor current is adiman, and output voltage is nipple free. The May. 2 duly natio" D" of the Greater for which the magnitude of the Steady state output voltage neman at 48v is ____ Conc-1:- Vs = 324, Vo = 484 $V_0 = \frac{V_{JD}}{1-P_1} => 48 = \frac{32 \times D_1}{1-P_1}$ 1-0 => 48-4801 = 3201

>> 0, = 0.6 ->0

$$\frac{Co_{1}-2}{=}:-V_{3}=72V, V_{0}=48V$$

$$=>48=\frac{72\times D_{2}}{1-D_{2}}$$

$$=>D_{2}=\frac{48}{72+48}=\frac{48}{120}=\frac{2}{5}\rightarrow0$$

$$D_{1} \leq D \leq D_{2}$$

$$\Rightarrow 0.6 \leq D \leq \frac{2}{5}$$

Buck - Boost Griter

(1) A buck-boost de-de Gavater Sham 81 uned to Gavat 244 battery voltege to 364 de voltage lè fue a load of 72W. It is operated at 20 kHz. Wilt an Inductor of 2 unit and output capacitance of 1000 MF. All devices age Gavidence le be Ideal. The peak voltage across the Solid - State Junks, (S) Th volts is ---



Sw-ON => S.c. => Vsw = 0 Veltage across the switch can be obtained only when the switch i off.



- >> -24 You (att) -36=0
 - >> Viw (off) = 24+36

(i) Extrus Quartiens :-
(i) Extrus Quartiens :-
(i)
$$V_0 = \frac{V_J D}{1-D}$$

(i) T_1
(i) T_2
(i) T_3
(i) T_4

B

$$I_{\perp}(av_{3}) = \frac{I_{0}}{I-P} = \frac{2}{I-O+6} = 5 \operatorname{Aup}$$

$$I_{\perp}(uax_{3}) = I_{\perp}(av_{3}) + \underbrace{\Delta I_{\perp}}_{2}$$

$$= 5 + \underbrace{O\cdot36}_{2} = 5 + O\cdot18$$

$$= 5 \cdot 18 \operatorname{Aup}$$

$$I_{\perp}(uuin) = I_{\perp}(av_{3}) - \underbrace{\Delta I_{\perp}}_{2}$$

$$= 5 - \underbrace{O\cdot36}_{2} = 5 - O\cdot18$$

$$= 4 \cdot 82 \operatorname{Aup}$$

$$(V_{5} I_{5} = V_{0} I_{0}$$

$$= 3 I_{5} = 3 \operatorname{Aup}$$

(12) A voltage commutates chopper openting of a de motol as shown in the form. (a) Minimum turn-on lime of "M", at IKH3 is uses to Gestas) the Speed The load current is assumed to be = A, 200 ×10 ×1×10 Contant at 10 Amp. = 140 Msec (a) The minimum time in Msee for which the SCR"M" Should (b) Vo (avg) = Vs x ty + 1 (2vs) x 2tem be ON is -Where, (b) The average output voltage tz= twin = T JLc= 140 MSec of a chopper will be -T = 1/f = 1/1×1000 tim = C.Vs = 1x10 x250 10 25 Msec => Vo (avg) = 47.5 V

(13) A Voltage commutated choppen ciquit Operated at 500 H3 is shann below. If the maximum value of load current is 10 Amp, then the maximum Current through the man (M) and Amilian (A) the man (M) and Amilian (A)





14) Consider the chopper Circuit shows To the foure. The chopper operates at 400 Hz and 50% duty cycle. The load current geneins almost nipple free at 10 Amp. Assuming the Juput voltage to be 200 v and the devices to be Ideal, the twon-off time available to the thysister, The is - Mac.



Cincust turn of thus of M",

$$T_{M} = t_{CM} = \frac{CV_{S}}{T_{0}} = \frac{2 \times 10^{6} \times 200}{10}$$

$$= 40 \text{ Msec}$$

(5) Fijure shows a Choppen. The desice "S," is the main switching device. "Sz" is the auxiliary commutation denice . "S," is gates for yoor, 60 Amp . "S2" is match for year, 30 Amp. The load curryent is 20 Aup. The main device opentes with a duly natio of o.s. The Peak current through "S," is ____

$$S_{1}(pert) = T_{TM}(pert) = T_{0} + \sqrt{3} \int_{L}^{C}$$

= 20 + 200 $\int_{200 \times 10^{-1}}^{2 \times 10^{-1}}$
= 40 Amp
$$T_{32}(pert) = T_{TA}(pert) = T_{0}$$